### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No	
Patent Issue Date	
Application Scrial No	
Filing Date	
Assignee	Micron Technology Inc
Inventorship	James E. O'Toole et al.
Attorney's Docket No	MI40-325
Title:	

### REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR APPLICANT MISTAKES and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)

To:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

ATTN:

Decision and Certificate of Correction

Branch of the Patent Issue Division

From:

D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,947,513 B2, granted September 20, 2005, in accordance with the Certificate of Correction form attached hereto in duplicate.

It is noted that errors appear in this patent of a typographical nature of character, as more fully described below. The errors occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require reexamination.

Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto, is Form PTO-1050, being suitable for printing.

The exact page and line number where the errors occur in the application file are:

- Page 43, line 12;
- Page 58, line 6;
- Page 65, line 10;
- Page 66, line 8;
- Page 66, line 11;
- Page 66, line 12;
- Page 89, line 5;
- Page 98, line 1;
- Page 103, line 21;
- Page 108, line 7;
- Page 114, line 21;
- Page 115, line 13;
- Page 115, line 15;
- Page 122, line 2;
- Page 122, line 15;
- Page 132, line 1;
- Page 137, line 23;
- Page 138, line 1;
- Page 146, line 8;
- Page 146, line 16;
- Page 148, line 23;
- Page 150, line 10;
- Page 165, line 21;
- Page 176, line 12;
- Page 185, line 2;
- Page 187, line 9;
- Page 192, line 23;
- Page 194, line 5;

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Page 194, line 7;
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Page 194, line 18;

Page 198, line 9;

Page 201, line 17;

Page 217, line 14;

Page 220, line 12;

Page 227, line 20;

Page 233, line 18;

Page 238, line 19;

Page 240, line 10;

Page 246, line 16;

Page 250, line 21;

Page 260, line 15;

Page 271, line 12;

Page 274, line 20;

Page 274, line 21;

Page 275, line 13;

Page 277, line 15;

Reference AR on the Form PTO-1449 filed February 20, 2002;

Claim 309 on page 17 of the Preliminary Amendment filed as a Request For Continued Examination filed September 20, 2004;

Claim 312 on page 19 of the Preliminary Amendment filed as a Request For Continued Examination filed September 20, 2004.

Please charge the fee specified under 37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925.

Respectfully submitted,

D. Brent Kenady Reg. No. 40,045

# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 6,947,513 B2

DATED

: September 20, 2005

INVENTOR(S)

: O'Toole et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page 1, OTHER PUBLICATIONS, Woods Reference, line 1, please delete "slef" after "scale" and insert --self--.

Col. 1, line 48, please delete "As" after "of".

Col. 2, line 45, please delete "Ghz." After "above 1" and insert -- GHz.--.

Col. 4, line 43, please delete "I" before "Another".

Col. 9, line 1, please delete "time" after "predetermined".

Col. 12, line 1, please delete "that" after "values" and insert --than--.

Col. 12, line 26, please delete "that" after "values" and insert --than--.

Col. 12, line 30, please add paragraph indentation before "Another".

Col. 12, line 32, please delete "selectively" before "voltage" and insert --selective--.

Col. 21, line 56, please insert --, now U.S. Pat. No. 5,790,946;-- after "Apr. 19, 1995".

Col. 21, line 57, please delete ", now U.S. Pat. No. 5,790,946:" after "System".

Col. 21, line 64, please delete "REID" after "Device" and insert --RFID--.

Col. 25, line 34, please delete "transistor" before "are" and insert --transistors--.

Col. 27, line 58, please delete "this" after "command." and insert -- This--.

Col. 29, line 36, please insert --be-- after "not".

Col. 32, line 12, please delete "bites" after "256" and insert "bytes".

Col. 32, line 30, please delete ""ramdch."" Before "In" and insert -- "rampch." --.

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### **Mailing Address of Sender:**

D. Brent Kenady Wells St. John P.S. 601 West First Avenue, Suite 1300 Spokane, WA 99201-3828

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PATENT NO.

: 6,947,513 B2

DATED

: September 20, 2005

INVENTOR(S)

: O'Toole et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 32, line 32, please delete "provides" after "circuits" and insert --provide--.

Col. 35, line 12, please delete "alubacell" after "cell" and insert -- "aluacell" --.

Col. 35, line 30, please delete "alubyt1" after "byte" and insert -- "alubyt1"--.

Col. 39, line 19, please delete "diodet" after "detector" and insert -- "diodedet" --.

Col. 41, line 44, please delete "Rick Lake" after "listing" and insert -- Rickie C. Lake--.

Col. 41, line 48, please delete ""diodet"" after "detector" and insert -- "diodedet"--.

Col. 42, line 14, please delete "dataln" after via and insert - dataIn"--.

Col. 45, line 4, please insert --as-- after "acts".

Col. 45, line 11, please insert --to-- after "relative".

Col. 46, line 9, please insert --to-- after "relative".

Col. 46, line 51, please delete "a" after "holes" and insert -- are --.

Col. 47, line 46, please delete "REFF" after "resistance" and insert -- REFF---.

Col. 48, line 22, please delete "7" after "resistor".

Col. 51, line 43, please delete "In ((W/L)Q1)/W/L)Q2)." After "(kT/qR)" and insert --In ((W/L)Q1)/W/L)Q2).--

Col. 53, line 5, please delete "Another other" after "20%." and insert -- Other--.

Col. 57, line 26, please delete "To" after "as" and insert -- $T_0$ --.

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PATENT NO.

: 6,947,513 B2

**DATED** 

: September 20, 2005

INVENTOR(S)

: O'Toole et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 57, line 45, please delete "millivolts" after "hundred" and insert --milliVolts--.

Col. 57, line 46, please delete "millivolts" after "of" and insert --milliVolts--.

Col. 57, line 48, please delete "millivolts" after "few" and insert --milliVolts--.

Col. 57, line 48, please delete "millivolts)," after "two" and insert --milliVolts),--.

Col. 57, line 49, please delete "millivolts." after "of" and insert --milliVolts.--.

Col. 60, line 59, please delete "show" after "708 is" and insert --shown--.

Col. 61, line 51, please delete "error" after "as" and insert --errors--.

Col. 62, line 62, please delete " $ENDT = Q1'Q0 \cdot TX'$ " and insert --  $ENDT = Q1'\cdot Q0 \cdot TX'$ ---

Col. 64, line 6, please delete "an" after "and" and insert --a--.

Col. 64, line 41, please delete "course" after "The" and insert --coarse--.

Col. 64, line 43, please delete "course" after "the" and insert --coarse--.

Col. 64, line 56, please delete "configuration" after "other" and insert --configurations--.

Col. 65, line 12, please delete "n+and p+active" after "minimizing" and insert --n+ and p+ active--.

Col. 66, line 23, please insert --of-- after "side".

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PATENT NO.

: 6,947,513 B2

DATED

: September 20, 2005

INVENTOR(S)

: O'Toole et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 67, line 52, please delete "embodiment" after "In" and insert --embodiments--.

Col. 74, line 16, please delete "include" before "a bias" and insert --includes--.

Col. 75, line 24, please delete "is" after "or" and insert --it--.

Col. 77, line 26, please delete "adaNew." after "converter" and insert -- "ada\_New"--.

Col. 78, line 28, please delete "up." After "power" and insert --up--.

Col. 78, line 29, please delete "operation," after "of" and insert --operations,--.

Col. 80, line 56, please delete "current" after "current".

Col. 82, line 59, please delete "10AA-DI" after "FIGS." and insert --10AA-DD--.

Col. 83, line 38, please delete ""dcor\_bias"" after "generator" and insert -- "dcorr\_bias"--.

Col. 85, line 62, please delete "pulidown" after "static" and insert --pulldown--.

Col. 86, line 5, please delete ""padigin"" after "buffer" and insert -- "paddigin" --.

Col. 86, line 21, please delete "Asl" before "and" and insert -- As1--.

Col. 87, line 36, please delete "so" after "tsn".

Col. 87, line 50, please delete "FIG." before "19AA-EK" and insert --FIGS.--.

Col. 88, line 27, please delete "so" before "work".

Col. 88, line 65, please delete ""Dataln"" after "line" and insert -- "DataIn"--.

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Patent No. <u>6,947,5</u>13 B2

### UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 6,947,513 B2

DATED

: September 20, 2005

INVENTOR(S)

: O'Toole et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 91, line 33, please delete "TagStoredInterrid" after "LswTagId" and insert --TagStoredInterrId --.

Col. 95, line 40, please delete "an" after "622" and insert --a--.

Col. 96, line 59, please delete "(RandomValueId)" after "value ID" and insert --(RandomValueId)--.

Col. 96, line 61, please delete "RandomValueld" before "is set" and insert --RandomValueId--.

Col. 97, line 11, please delete "RandomValueld." after "the" and insert --RandomValueId.--.

Col. 97, line 36, please delete "RandomValueId." before "In" and insert --RandomValueId.--.

Col. 98, line 1, please delete "use" after "be" and insert --used--.

Col. 103, line 23, claim 32, please delete "a" after "using".

Col. 104, line 12, claim 35, please insert --claim-- after "with".

Page 5 of 5

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